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Sheet	1	of	1	Attorney Docket Number	138.0001
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Complete if Known

Application Number	10/648,154
Filing Date	08/26/2003
First Named Inventor	Pechanek
Art Unit	
Examiner Name	

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<i>la</i>		BLAUUW ET AL., Computer Architecture: Concepts and Evolution, 1997, Page(s) 71-75, 589-590, 648-664, Publisher: Addison-Wesley, Published In: Reading, Mass.	
<i>h</i>		JOHNSON, An Introduction to Vector Processing, Computer Design, February 1978, Page(s) 89-97	
<i>2</i>		PECHANЕК ET AL., The ManArray Embedded Processor Architecture, Proceedings of the 26th EUROMICRO Conference, 2000, Page(s) 348-355, Publisher: IEEE Computer Society	
<i>2</i>		PELEG ET AL., Intel MMX for Multimedia PCs, Communications of the ACM, January 1997, Page(s) 25-38, Volume 40, Number 1	

Examiner Signature	<i>[Signature]</i>	Date Considered	<i>3/14/03</i>
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